

65/030134

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SERIAL NUMBER 1030134	FILING DATE 10/21/97	CLASS 662	SUBCLASS 662
		GROUP ART UNIT 2134	EXAMINER H. M. Z.

APPLICANTS
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SEARCHING DATA.....
 INVENTION THIS APPLN IS A CIP OF 07/586,758 09/24/90 PAT 5,148,744
 AND WHICH IS A CIP OF 07/673,020 03/21/91 PAT 5,148,261
 AND A CIP OF 07/765,928 09/24/91 PAT 5,347,154
 CON

SEARCHED/PCD APPLICATIONS.....
 VERIFIED PCD PLT/US91/06920 09/24/91
 200

NO DECLARATION
 No term of this patent shall not exceed
 beyond the expiration date
 of Pat. No. 5148261

FOREIGN FILING LICENSE GRANTED 07/23/93

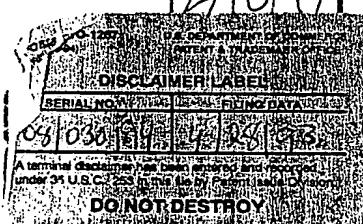
First priority claimed 35 U.S.C. 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	AS FILED	STATE OR COUNTRY NY	SHETS DRWGS. 19	TOTAL CLAIMS 60	INDEP. CLAIMS 11	FILING FEE RECEIVED \$3,112.00	ATTORNEY'S DOCKET NO. TESSERA3,300
Verifier and Acknowledged Examiner's Initials LENNER, DAVID, LITTENBERG, KRUMHOLZ & LENTLIK #30 SOUTH AVENUE WEST WESTFIELD, NJ 07090							

SEMICONDUCTOR CHIP ASSEMBLIES, METHODS OF MAKING SAME AND COMPONENTS FOR SAME

U.S. DEPT. OF COMM-PAT. & TM OFFICE—PTO-435L (Rev. 10-78)

PARTS OF APPLICATION
FILED SEPARATELY

NOTICE OF ALLOWANCE MAILED 1/22/97	PREPARED FOR ISSUE S. V. Chuk Assistant Examiner	CLAIMS ALLOWED Total Claims 2730
ISSUE FEE OUNT DUE 2-9080 Date Paid 2/10/97	Docket Clerk Mahshid Saadat MAHSID D. SAADAT SUPERVISORY PATENT EXAMINER GROUP 2500 Primary Examiner	DRAWING Sheets Drwg. 19
	ISSUE CLASSIFICATION 88	Print Claim Figs. Drwg. 35
	ISSUE BATCH NUMBER I71	Print Fig. 2
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US91/06920		OCT 21 1991	PATENT NUMBER <i>5674977</i>
APPLICATION NUMBER 14/030, 194	FILED DATE 04/28/93	CLASS 10	SUBCLASS 10

IGOR V. KHANDEEV, PEEKSKILL, NY; THOMAS H. DISTERHOFT, NEW YORK, NY.

CONTINUING DATA***

VERIFIED THIS APPLN IS A CIP OF 07/586,780 filed 07/23/91 Prior Art
 AND WHICH IS A CIP OF 07/573,020 filed 24/01/91 Prior Art
 AND A CIP OF 07/765,928 filed 04/24/91 Prior Art
CON

FOREIGN/PCT APPLICATIONS***

VERIFIED PCT PCT/US91/06920 09/24/91

NOTICE OF DISCLAIMER

The term of this patent shall not extend beyond the expiration date of Pat. No. *514326*.

EXAMINER FILING LICENSE GRANTED 07/23/93

Nonprovisional claim(s) made under 35 U.S.C. 111(a)(1) or 111(a)(2) and 37 CFR 1.55(b) and (c) conditions met. <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	AS FILED	STATE OR COUNTRY NY	SHEETS DRWGS. 19	TOTAL CLAIMS 60	INDEP. CLAIMS 11	FILING FEE RECEIVED \$3,112.00	ATTORNEY'S DOCKET NO. TESSERA 3,800
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Examiner: *JOHN R. LITTENBERG, KRUMHOLZ &*

1000 AVENUE WEST
07090

ARTICLE FOR CHIP ASSEMBLIES, METHODS OF MAKING SAME AND COMPONENTS

U.S. DEPT. OF COMM., PAT. & TM OFFICE — PTO-436L (REV. 10-78)

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner) _____ (Date) (Legal Instruments Examiner) _____ (Date)			ISSUE FEE	
				Amount Due	
				ISSUE BATCH NUMBER	

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